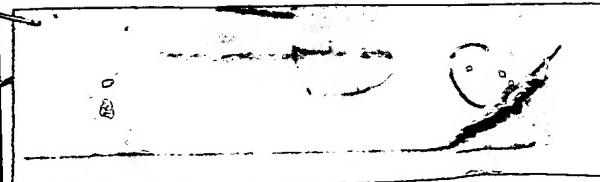


10/12/00  
109/687524  
1C921 U.S. PRO

228	214	Subclass	<del>ISSUE CLASSIFICATION</del>
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**PATENT NUMBER**

6585150



**U.S. UTILITY PATENT APPLICATION**

*MAT* O.I.P.E.  
SCANNED *Anz* Q.A. *WJ*

**PATENT DATE**

JUL 01 2003

APPLICATION NO. 09/687524	CONT/PRIOR	CLASS 228	SUBCLASS 214	ART UNIT 1725	EXAMINER Johnson
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PLICANTS William Bernier  
Donald Henderson  
James Spalik  
Isabelle Paquin

Solder protective coating and fluxless joining of flip chip devices  
on laminates with plated solder

TITLE

PTD-2040  
12/99

**PREPARED AND APPROVED FOR ISSUE**

## **ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.	<i>Jonathan J. Jr.</i> (Assistant Examiner)			15	1
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	<i>TOM DUNN</i> TOM DUNN SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 1700  (Primary Examiner)			12/25/03 (Date)	12/27/03 <i>MP</i>
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	<i>DAN</i> (Legal Instruments Examiner)			3/4/03 (Date)	\$1300 4/21/03 ISSUE BATCH NUMBER
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**ISSUE FREE IN FILE** (LABEL AREA)

(FACE)